



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5  
AFT 7/15/03  
7/15/03

Applicant: Jie Liang  
Serial No: 09/517,234  
Filed: 03/02/2000  
Title: Image Coding Using Embedded Zerotree Patterns and Bitplanes  
Art Unit: 2624  
Examiner: A. Do  
Docket No.: TI-29011  
Conf. No.: 4622

RECEIVED

JUL 15 2003

Technology Center 2600

**NOTICE OF APPEAL**

Assistant Commissioner  
for Patents  
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
<u>Gracia Sansom</u> Gracia Sansom	<u>7-7-03</u> Date

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner dated **04/07/2003** all rejected claims.

The item(s) checked below are appropriate:

1. \_\_\_\_\_ An extension of time to respond to the final rejection  
\_\_\_\_\_ was granted on  
\_\_\_\_\_ is requested for month(s).

2. \_\_\_\_\_ A timely response to the final rejection has been filed.

3.   X   Fee \$320.00:


\_\_\_\_\_ Not required (Fee paid in prior appeal).

  X   The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to the Deposit Account of Texas Instruments Incorporated, Account No. 20-0668. An original and one copy of this sheet are enclosed.

07/14/2003 AWONDAF1 00000072 200668 09517234  
01 FC:1401 320.00 DA

Signature [Rule 191(b)]

Post Office Address  
(to which correspondence is to be sent)

  
Carlton H. Hoel  
Registration No. 29,934  
Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265  
(972) 917-4365